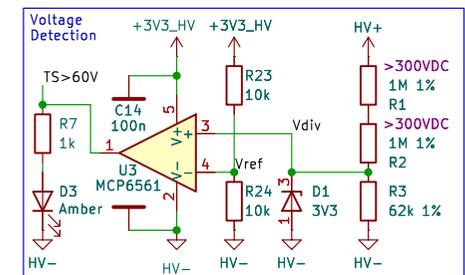
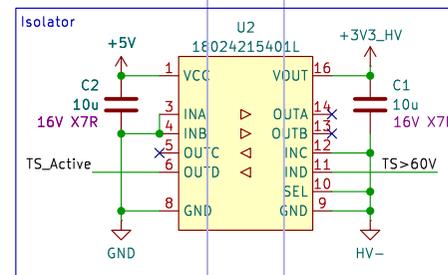
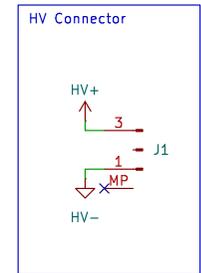
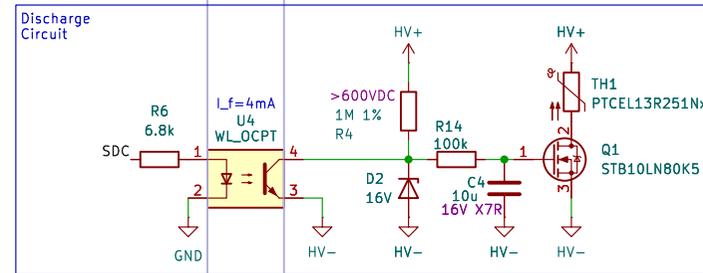
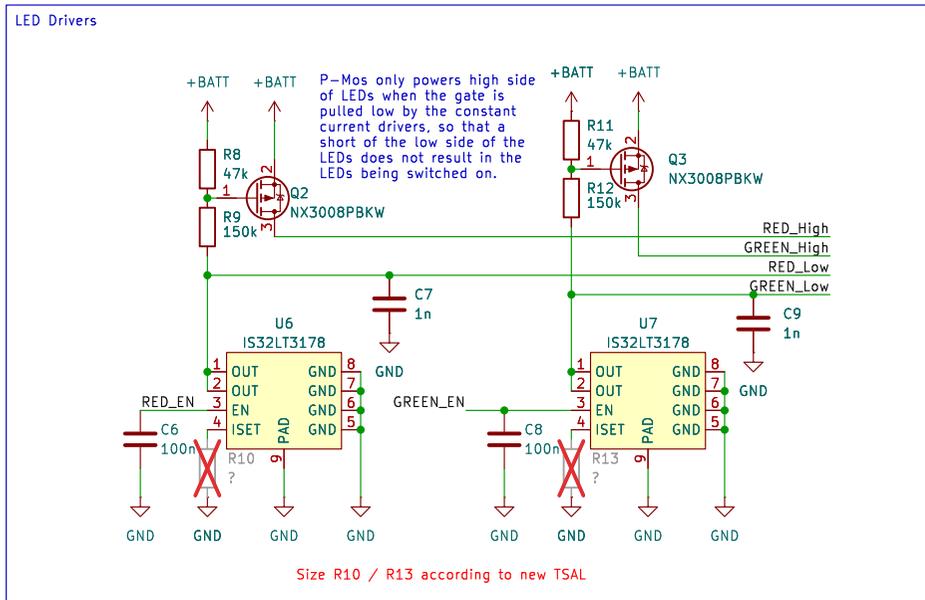
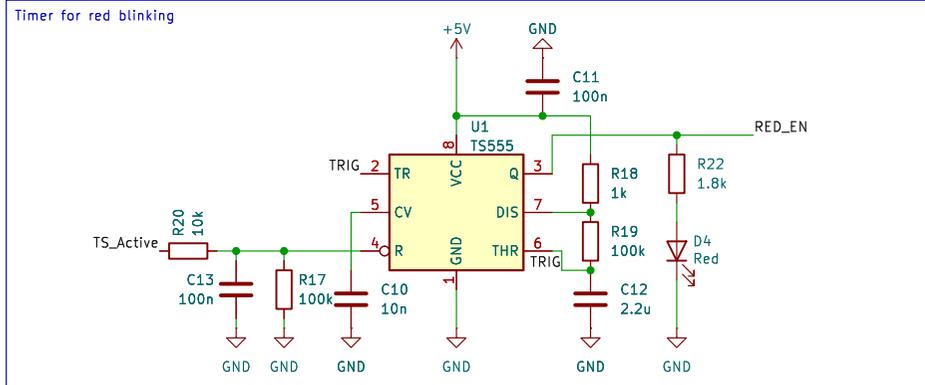


# TSAL driver board and DC-link voltage detection

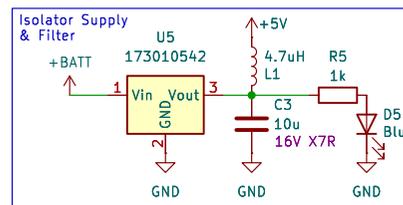
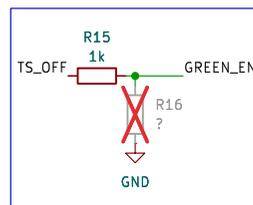
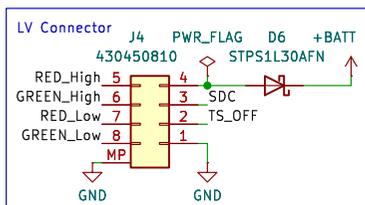
LV TS



$$Vref\_max = 3.47V * 10.1k\Omega / (10.1k\Omega + 9.9k\Omega) = 1.76V$$

$$Vdiv@60V\_min = 60V * 61.38\Omega / (2 * 990k\Omega + 61.38k\Omega) = 1.8V$$

$$Vhyst\_max@75deg = 6mV < 40mV (1.80V - 1.76V)$$



Sheet: /		Date:	
File: DC.kicad_sch		Rev:	
Size: A4	KiCad E.D.A. 9.0.1		Id: 1/1